

READ ONLY MEMORY DEVICE

ABSTRACT

The present invention relates to an improved read only memory device. The read
5 only memory device includes a read only memory cell array with a plurality of first read only
memory cells and a plurality of second read only memory cells. A reference memory cell
array includes a plurality of first reference memory cells and at least one second reference
memory cell. A dummy memory cell array includes a plurality of first dummy memory cells
and a plurality of second dummy memory cells. A reference word line selecting circuit
10 selects the reference word line responsive to a row address.